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Won-Sung Sohn, Seung-Kyu Ko, Yoon-Chui Choy, Kyong-Ho Lee, Sung-Hyuk Kim, Soon-Bum Lim
March 2002 **Proceedings of the 2002 ACM symposium on Applied computing**
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2 Semantic search on Internet tabular information extraction for answering queries   ◻
H. L. Wang, S. H. Wu, I. C. Wang, C. L. Sung, W. L. Hsu, W. K. Shih
November 2000 **Proceedings of the ninth international conference on Information and knowledge management**
Full text available: 🔳 pdf(197.52 KB)   Additional Information: full citation, references, index terms

> **Keywords**: data mining, information extraction, semantic query, table understanding

3 Parallel algorithms for geometric searching problems   ◻
S.-J. Oh, M. Suk
August 1989 **Proceedings of the 1989 ACM/IEEE conference on Supercomputing**
Full text available: 🔳 pdf(741.72 KB)   Additional Information: full citation, abstract, references, index terms

> Computational Geometry is concerned with the design and analysis of computational algorithms which solve geometry problems. In this paper SIMD-MCC algorithms for solving geometric searching problems such as the point domination, the point maxima, the range searching, and the closest point problems are designed by using the parallel divide-and-conquer technique. The computational complexities of all these algorithms for N input points in the k-dimensional space are O(CkN ...

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1 Color graphics for remote teaching ▣
William Hankley, Virgil Wallentine
July 1980　**ACM SIGGRAPH Computer Graphics , Proceedings of the 7th annual conference on Computer graphics and interactive techniques**, Volume 14 Issue 3
Full text available: 🔲 pdf(366.69 KB)　Additional Information: full citation, abstract, references, index terms

A system for preparation and manipulation of color graphics video frames is described. The system commands are designed for use by a class instructor (who is not a computer science person) in preparation and delivery of lectures to a remote site. The system can provide the types of images, overlays, and coloring that could be drawn using transparency sheets and color pens, or that could prepared as 35 mm technical slides (not photographs, but text and diagrams). The hardware consists of sta ...

Keywords: Color graphics, Teaching support

2 KMS: a distributed hypermedia system for managing knowledge in organizations ▣
Robert M. Akscyn, Donald L. McCracken, Elise A. Yoder
July 1988　**Communications of the ACM**, Volume 31 Issue 7

Full text available: 🔲 pdf(1.67 MB)　Additional Information: full citation, abstract, references, citings, index terms, review

Developers of hypermedia systems face many design issues. The design for KMS, a large-scale hypermedia system for collaborative work, seeks improved user productivity through simplicity of the conceptual data model.

3 Visualizing digital library search results with categorical and hierarchical axes ▣
Ben Shneiderman, David Feldman, Anne Rose, Xavier Ferré Grau
June 2000　**Proceedings of the fifth ACM conference on Digital libraries**

Full text available: 🔲 pdf(682.87 KB)　Additional Information: full citation, abstract, references, citings, index terms

Digital library search results are usually shown as a textual list, with 10-20 items per page. Viewing several thousand search results at once on a two-dimensional display with continuous variables is a promising alternative. Since these displays can overwhelm some users, we created a simplified two-dimensional display that uses categorical and hierarchical axes, called hieraxes. Users appreciate the meaningful and limited number of terms on each hieraxis. At each grid point ...

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Terms used <u>layout</u> <u>flow</u> <u>vertical</u>   Found **12,252** of **134,837**

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181 <u>ALPS2: a standard cell layout system for double-layer metal technology</u>   ■

C. P. Hsu, B. N. Tien, K. Chow, R. A. Perry, J. Tang
June 1985 **Proceedings of the 22nd ACM/IEEE conference on Design automation**

Full text available: 📄 pdf(605.60 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

A layout style for double-layer metal standard cell design is described. Based on this particular style, an automatic layout system (ALPS2) is developed. It mixes state of the art placement and routing techniques in different stages of the layout process, which deviates from the traditional place-first-then-route strategy, and produces efficient results. The chip area comparisons between three single layer metal standard cell designs and those of the ALPS2 system showed up to 18% reduction ...

182 <u>Automatic generation of digital system schematic diagrams</u>   ▬

Anjali Arya, Anshul Kumar, V. V. Swaminathan, Amit Misra
June 1985 **Proceedings of the 22nd ACM/IEEE conference on Design automation**

Full text available: 📄 pdf(639.41 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

This paper presents a rigorous approach to automatic generation of schematic diagrams for digital systems described as networks of modules. This is very useful in comprehensive CAD environment. The approach is based on identification of some guidelines which are traditionally followed in manual drawing of schematic diagrams. Theses guidelines are transformed into quantitative objectives. In view of the complex interrelationship between these objectives, the schematic design process is broke ...

183 <u>Yet another silicon compiler</u>   ▬

David E. Krekelberg, Gerald E. Sobelman, Chu S. Jhon
June 1985 **Proceedings of the 22nd ACM/IEEE conference on Design automation**

Full text available: 📄 pdf(731.46 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

In this paper, we describe the YASC high-level silicon compiler which synthesizes compact chip layouts from hierarchical behavioral descriptions. A logic synthesis procedure generates sets of Boolean equations, including multi-phase clocks and any necessary interface logic. A novel technique for layout generation yields cells whose densities approach hand-crafted designs. Two-layer metal NMOS and CMOS technologies are supported, with flexible design rules. In addition to layout synthesis, I ...